

PQ30RV31

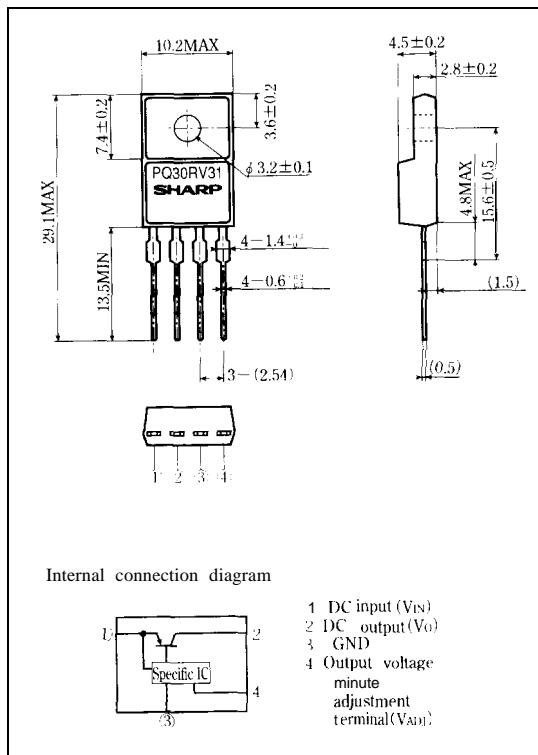
Variable Output Low Power-Loss Voltage Regulator

■ Features

- Maximum output current: 3A
- Compact resin full-mold package.
- Low power-loss (Dropout voltage: MAX. 0.5V)
- Variable output voltage (setting range :1.5 to 30V)
- Built-in ON/OFF control function.

■ Outline Dimensions

(Unit: mm)



■ Applications

- Power supply for print concentration control of word processors
- Series power supply for motors and solenoid
- Series power supply for VCRs and TVs

■ Absolute Maximum Ratings

(T_a=25°C)

| Parameter | Symbol | Rating | Unit |
|---|------------------|---------------|------|
| * 1 Input voltage | V _{IN} | 35 | V |
| *1 Output adjustment terminal voltage | V _{ADJ} | 7 | V |
| Output current | I _O | 3 | A |
| Power dissipation (No heat sink) | P _{D1} | 2.0 | W |
| Power dissipation (With infinite heat sink) | P _{D2} | 20 | W |
| *2 Junction temperature | T _J | 150 | °C |
| Operating temperature | T _{opr} | -20 to +80 | °C |
| Storage temperature | T _{stg} | -40 to +150 | °C |
| Soldering temperature | T _{sot} | 260 (For 10s) | °C |

*1,411 are open except GND and applicable terminals.

*2 Overheat protection function may operate at 125 ≤ T_J ≤ 150 °C

Please refer to the chapter "Handling Precautions"

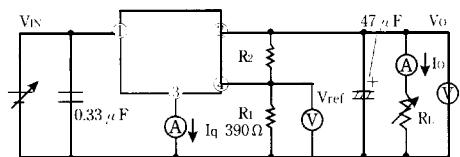
SHARP

■ Electrical Characteristics (Unless otherwise specified, condition shall be $V_{IN}=12V$, $V_0=10V$, $I_0=1.5A$, $R_L=390\Omega$, $T_a=25^\circ C$)

| Parameter | Symbol | Conditions | MIN. | TYP. | MAX. | Unit |
|--|----------------|--|-------|-----------|-------|---------------|
| Input voltage | V_{IN} | | 4.5 | | 35 | v |
| output voltage | V_0 | | 1.5 | | 30 | V |
| Load regulation | Reg_L | $I_0=5\text{mA}$ to 3A | 0.5 | 2.0 | 2.0 | % |
| Line regulation | Reg_I | $V_{IN}=11$ to 21V , $I_0=0.5\text{mA}$ | 0.5 | 2.5 | 2.5 | % |
| Ripple rejection | RR | Refer to Fig. 2 | 45 | 70 | | dB |
| Reference voltage | V_{ref} | | 1.225 | 1.25 | 1.275 | V |
| Temperature coefficient of reference voltage | $T_c V_{ref}$ | $T_a=0$ to $125^\circ C$, $I_0=5\text{mA}$ | | ± 1.0 | | %/ $^\circ C$ |
| Dropout voltage | V_{i-0} | *3, $I_0=3\text{A}$ | 0.3 | 1.0 | | v |
| Quiescent current | I_q | *4, $I_0=2\text{A}$ | 0.2 | 0.5 | | |
| | | $I_0=0$ | | | 7 | mA |

*3 Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

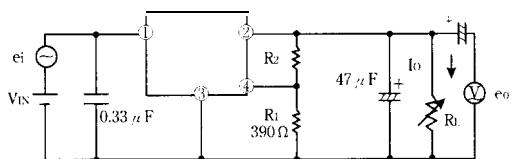
Fig. 1 Test Circuit



$$V_0 = V_{ref} \times \left(1 + \frac{R_2}{R_1} \right) \approx 1.25 \times \left(1 + \frac{R_2}{R_1} \right)$$

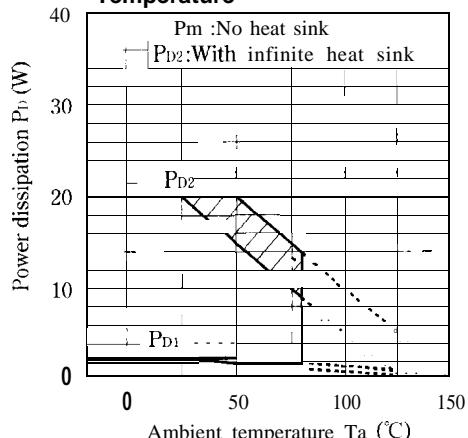
[$R_1=390\Omega$, $V_{ref}=1.25V$]

Fig. 2 Test Circuit of Ripple Rejection



$$\begin{aligned} I_0 &= 0.5\text{A}, V_{IN}=12\text{V}, V_0=10\text{V} \\ f &= 120\text{Hz} (\text{sine wave}) \\ e_i &= 0.5\text{Vrms} \\ RR &= 20 \log(e_i/e_o) \end{aligned}$$

Fig. 3 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion : Overheat protection may operate in this area.

Fig. 4 Overcurrent Protection Characteristics (Typical Value)

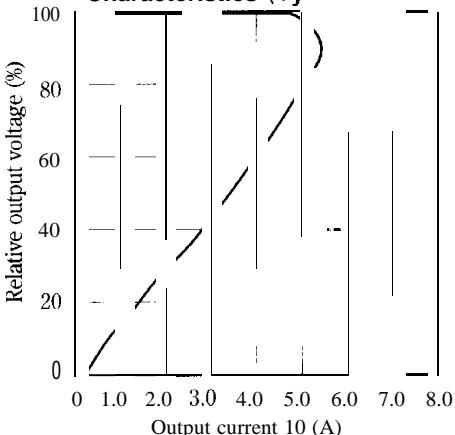


Fig. 5 Output Voltage Adjustment Characteristics (Typical value)

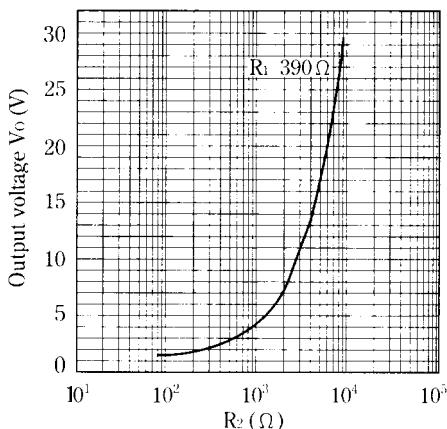


Fig. 7 Dropout Voltage vs. Junction Temperature

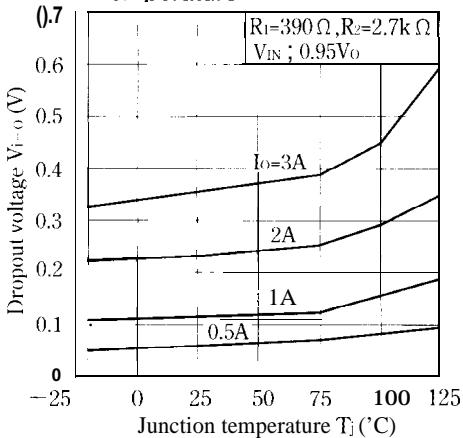


Fig. 9 Ripple Rejection vs. Output Current

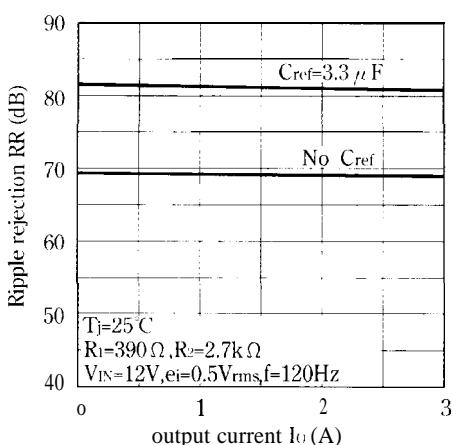


Fig. 6 Output Voltage vs. Input Voltage

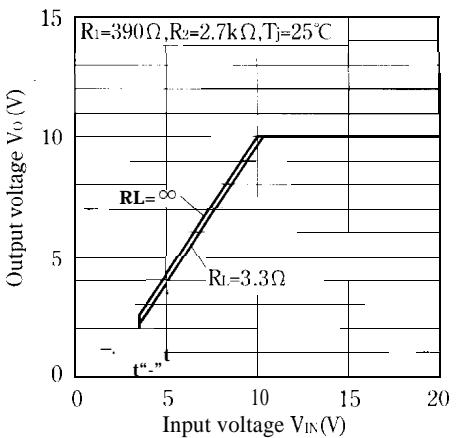


Fig. 8 Ripple Rejection vs. Input Ripple Frequency

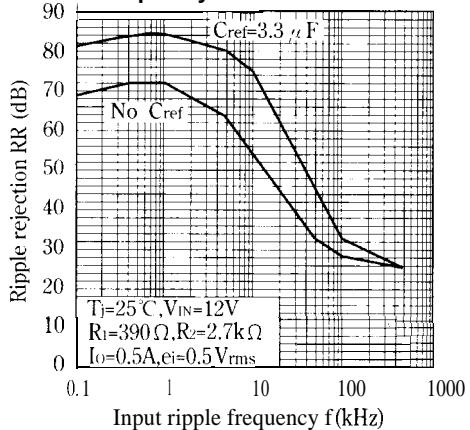


Fig. 10 Output Peak Current vs. Dropout Voltage (Typical)

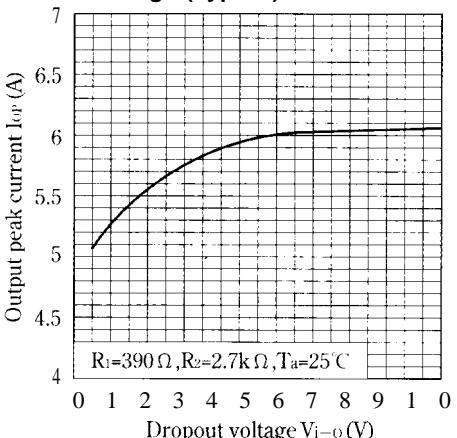
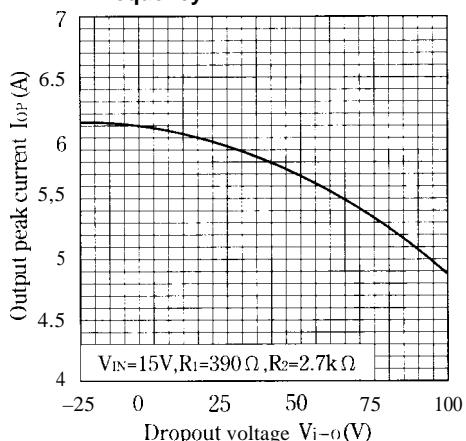
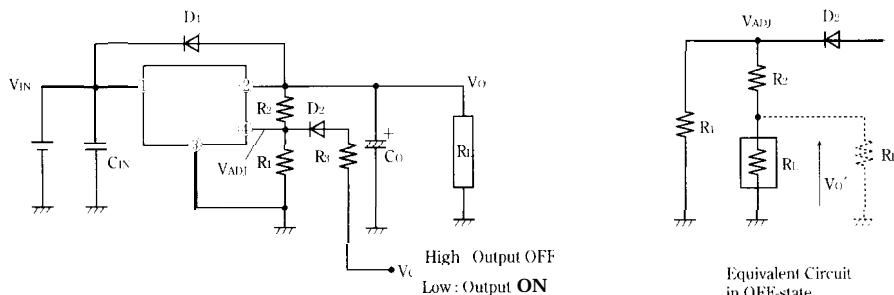


Fig. 8 Ripple Rejection vs. Input Ripple Frequency



■ ON/OFF Operation



- . ON/OFF operation is available by mounting externally D2 and R3.
- . When V_{ADJ} is forcibly raised above V_{REF} (1.25V TYP) by applying the external signal, the output is turned off (pass transistor of regulator is turned off). When the output is OFF, V_{ADJ} must be higher than $V_{REF\ MAX.}$, and at the same time must be lower than maximum rating 7V.
- . In OFF-state, the load current flows to R_L from V_{ADJ} through R_2 . Therefore the value of R_2 must be as high as possible.
- $V_O' = V_{ADJ} \times R_L / (R_1 + R_2)$ occurs at the load. OFF-state equivalent circuit R_1 up to 10k Ω is allowed. Select as high value of R_1 and R_2 as possible in this range. In some case, as output voltage is getting lower ($V_O < 1V$), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of V_O' . So add the dummy resistance indicated by R_d in the figure to the circuit parallel to the load.